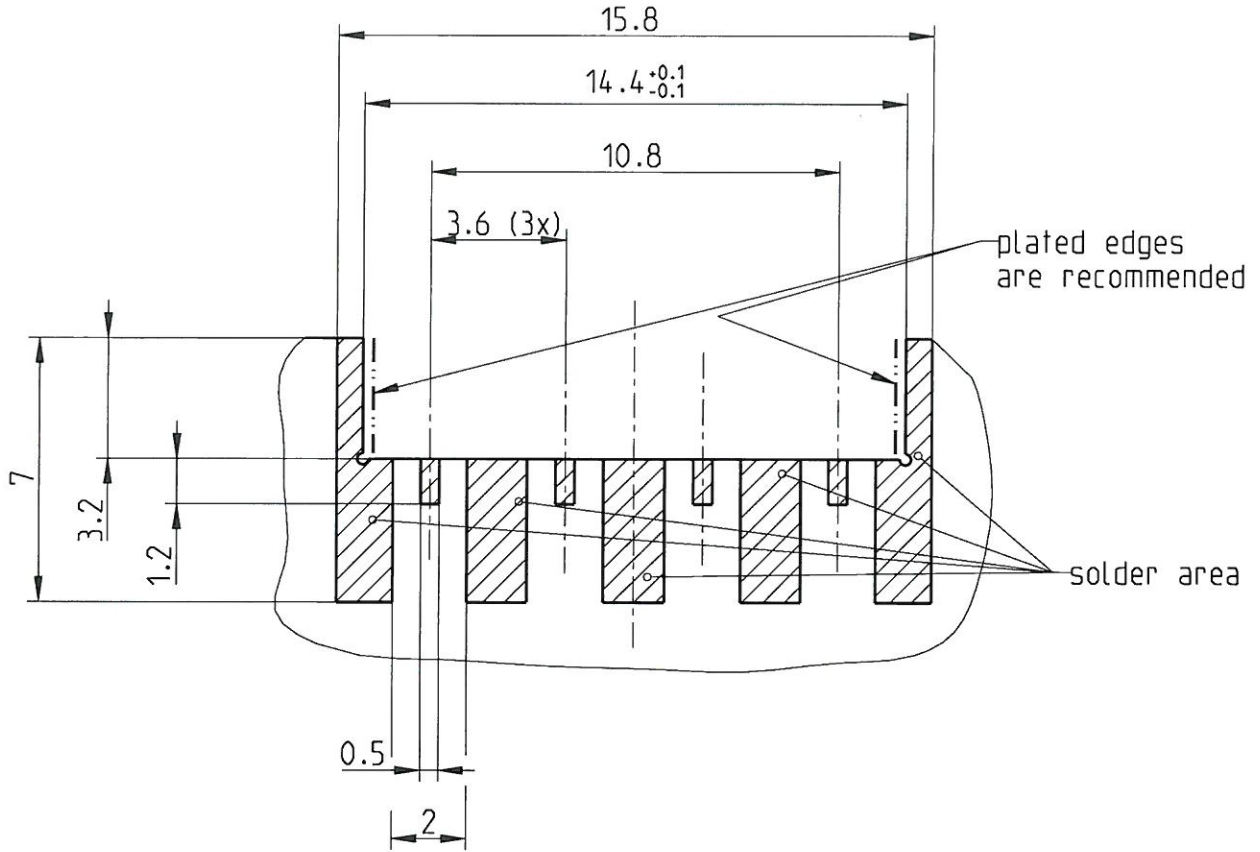


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Leiterplatten-Layout  
PCB layout  
B 339g



A wide variety of transmissionline topologies and pcb-parameters like permittivity, substrate thickness, and board-stackup are applied by customers. These parameters have a strong impact on the high frequency performance of the mounted connector.

Please note, that the given layout is not optimised to fit all of the possible board configurations regarding RF-performance, it represents a recommendation for optimum solderability of the connector.

In order to guarantee optimum high frequency properties of the connector, an RF-analysis of the connector to board transition is recommended.

Formzahl: TCC\_EB\_05\_PZ\_44\_Einzelteil  
Prod. i. Vorgefertigung  
Datei: AL\_EINZELTEIL\_EB\_05.RDR  
Version: 1,2

Dimensions  
in mm



ISO-Projektion  
Methode E

<b>Rosenberger</b> Hochfrequenztechnik 84526 Tittmoning Pro/ENGINEER				general tolerance ISO 2768 RN 006-01 m-H dimensions <0.5 and symmetry		scale: 5:1	weight[g]: surface[mm <sup>2</sup> ]:			
				drawn 20.09.2010 C_Vitzthum check. 30.9.10 <i>in F.</i> appr. 30.09.2010 <i>W...</i> dimensioning incl. finish		title: <b>Leiterplatten-Layout PCB layout</b>				
b00	10-0022	C_Vitzthum	30.09.2010	drawing-no...: <b>MB_339G</b>	sheet: 1					
a00	10-s632	C_Vitzthum	20.09.2010		of: 1					
rev.	change-no	name	date	distribu- tion to:	FE	AZ	QSM	RMT	.	remarks: .